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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/015,374	12/12/2001	Kwang Seok Oh	W2K1070	2810

23504 7590 08/11/2003

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[REDACTED] EXAMINER

WILLIAMS, ALEXANDER O

[REDACTED] ART UNIT [REDACTED] PAPER NUMBER

2826

DATE MAILED: 08/11/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.	OH ET AL.
10/015,374	
Examiner	Art Unit
Alexander O Williams	2826

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 27 May 2003.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-9, 12, 16, 21, 22 and 39-46 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-9, 12, 16, 21, 22 and 39-46 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

- Certified copies of the priority documents have been received.
- Certified copies of the priority documents have been received in Application No. _____.
- Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) Notice of References Cited (PTO-892)

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.

4) Interview Summary (PTO-413) Paper No(s). _____.

5) Notice of Informal Patent Application (PTO-152)

6) Other: _____.

Serial Number: 10/015374 Attorney's Docket #: WK2K1070
Filing Date: 12/12/01; claimed foreign priority to 3/9/2001

Applicant: Oh et al.

Examiner: Alexander Williams

Applicant's Amendment in Paper # 11, filed 5/27/03, has been acknowledged.

This application contains claims 43 to 46 drawn to an invention non-elected with traverse in Paper No. 6.

Claims 10, 11, 13-15, 17-20 and 23-38 have been canceled.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1 to 9, 12, 21, 22 and 39-46 are rejected under 35 U.S.C. 102(e) as being anticipated by Lo et al. (U.S. Patent Application Publication # 2002/0014689 A1).

For example, in claim 1 and similar claims 22 and 39, Lo et al. (figures 4A to 6C) specifically figure 6B show a semiconductor package comprising: a substrate **602**; a first semiconductor chip **608,606** coupled to a surface of the substrate, the first semiconductor chip having first and second surfaces which are substantially flat in nature; an adhesive layer coupled to the second surface of the first semiconductor chip; a second semiconductor chip **611,613** having first and second surfaces which substantially flat in nature; and an insulator **620** coupled to the first surface of the second semiconductor chip for preventing shorting of wirebonds wherein the second

semiconductor chip is coupled to the adhesive layer by the insulator coupled to the first surface thereof.

Applicant cannot rely upon the foreign priority papers to overcome this rejection because a translation of said papers has not been made of record in accordance with 37 CFR 1.55. See MPEP § 201.15.

Claims 1 to 9, 12, 21, 22 and 39-46 are rejected under 35 U.S.C. 102(e) as being anticipated by Foster (U.S. Patent # 6,437,449 B1).

For example, in claim 1 and similar claims 22 and 39, Foster et al. (figures 1 to 4) specifically figure 3 show a semiconductor package comprising: a substrate **204**; a first semiconductor chip **108** coupled to a surface of the substrate, the first semiconductor chip having first and second surfaces which are substantially flat in nature; an adhesive layer coupled to the second surface of the first semiconductor chip; a second semiconductor chip **140** having first and second surfaces which substantially flat in nature; and an insulator **216** coupled to the first surface of the second semiconductor chip for preventing shorting of wirebonds wherein the second semiconductor chip is coupled to the adhesive layer by the insulator coupled to the first surface thereof.

Applicant cannot rely upon the foreign priority papers to overcome this rejection because a translation of said papers has not been made of record in accordance with 37 CFR 1.55. See MPEP § 201.15.

Claims 1 to 9, 12, 21, 22 and 39-46 are rejected under 35 U.S.C. 102(b) as being anticipated by LoBianco et al. (U.S. Patent # 6,340,846 A1).

For example, in claim 1 and similar claims 22 and 39, LoBianco et al. (figures 1 to 8) specifically figure 8 show a semiconductor package comprising: a substrate **20**; a first semiconductor chip **14** coupled to a surface of the substrate, the first semiconductor

chip having first and second surfaces which are substantially flat in nature; an adhesive layer coupled to the second surface of the first semiconductor chip; a second semiconductor chip **16** having first and second surfaces which substantially flat in nature; and an insulator **50** coupled to the first surface of the second semiconductor chip for preventing shorting of wirebonds wherein the second semiconductor chip is coupled to the adhesive layer by the insulator coupled to the first surface thereof.

Response

Applicant's arguments filed 5/27/03 have been fully considered, but are moot in view of the new grounds of rejections detailed above.

Field of Search	Date
U.S. Class and subclass: 257/685,686,723,777,784,786	1/13/03 8/6/03
Other Documentation: foreign patents and literature in 257/685,686,723,777,784,786	1/13/03 8/6/03
Electronic data base(s): U.S. Patents	1/13/03 8/6/03

Papers related to this application may be submitted to Technology Center 2800 by facsimile transmission. Papers should be faxed to Technology Center 2800 via the Technology Center 2800 Fax center located in Crystal Plaza 4-5B15. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (November 15, 1989). The Technology Center 2800 Fax Center number is (703) 308-7722 or 24. Only Papers related to Technology Center 2800 APPLICATIONS SHOULD BE FAXED to the GROUP 2800 FAX CENTER.

Any inquiry concerning this communication or any earlier communication from the examiner should be directed to **Examiner Alexander Williams** whose telephone number is (703) 308-4863.

Any inquiry of a general nature or relating to the status of this application should be directed to the **Technology Center 2800 receptionist** whose telephone number is **(703) 308-0956**.

8/6/03



Primary Examiner
Alexander O. Williams